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Nicole Barrese 10/19/04
(Signature & date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of _____:

October 19, 2004

Gabillard, et al.:

Group Art Unit: 2817

Serial No.: 10/604,478

Examiner: Sharon Latimer
Confirmation No. 1477

Filed: 7/24/03 :

International Business Machines Corporation
2070 Route 52
Hopewell Junction, NY 12533

**TITLE: IMPROVED 2-STAGE LARGE BANDWIDTH AMPLIFIER USING
DIODES IN THE PARALLEL FEEDBACK STRUCTURE**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the US patents and publications as listed on Form PTO-1449, attached hereto.

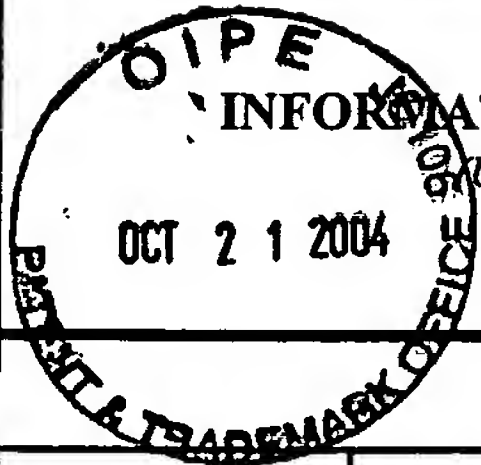
In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,
Gabillard, et al.

By *Lisa J. Ulrich*
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FR920020053US1



Docket Number (Optional) FR920020053US1		Application Number 10/604,478
Applicant(s) Gabillard, et al.		
Filing Date 7/24/03		Group Art Unit 2817

U.S. PATENT DOCUMENTS :

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,956,615	9/11/90	Böhme, et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		EP 0558 244 A1	22.02.93	EUROPE				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		"Tunable Linearity Characteristics of a DC-3 GHz InP HBT Active Feedback Amplifier", Kobayashi, et al., IEEE, October 1999, pp. 291-294
		High-Bit-rate, High-Input-Sensitivity Decision Circuit Using Si Bipolar Technology" by K.Ishii et al, IEEE Journal of Solid-State Circuits, vol 29, No 5, May 1994, pp. 546-549

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.